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Complete if Known

Application Number

10/765,531

Filing Date

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First Named Inventor

Burn Jeng Lin et al.

Art Unit

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Examiner Name

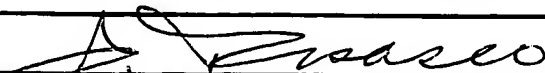
Stephen D. Rosasco

Attorney Docket Number

N1085.00225 (N1280-00235)

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A	MUTSUNORI IGARASHI ET AL., "A Diagonal-Interconnect Architecture and Its Application to RISC Core Design", ISSCC 2002, Session 12a, 3 pages	

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